## Course Title: Computer Organization

## Course Code: 12IS44

As a part of active learning and assignment component, students were divided into different groups of two and are asked to do study on advanced /research topics in the related area like advanced computer architectures, hyper threading etc.; The topics were finalized in the beginning of the semester so that the student can involve in preparations for the entire semester.

The work was evaluated for 10 marks. Students were asked to prepare a document which contained the results of their study after referring to standard journals papers. They were also asked to present the work that they had done. The rubrics used to evaluate the same is given in table 5.6.1.1

Rubrics for CO Assignment Evaluation

|  | Component (marks) | Excellent | Good | Not Satisfactory | Course Outcome |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A | Presentation (3 marks) | Concepts properly conveyed <br> (3 Mark) | Some parts were clear <br> (1.5 Mark) | Not understandable (0 Mark) | CO1 |
| B | Basic understanding and subject knowledge (3 marks) | Very good understanding (3 Mark) | Partial understanding of the problem (1.5 Mark) | No understanding. (0 Mark) | CO2, CO3 |
| C | Involvement in the work (2 marks) | Fully involved (2 marks) | Partially <br> (1 mark) | No involvement (0 Mark) | CO 3 |
| D | Selection and Relevance of the topic (2 marks) | Most relevant <br> Topic (2 Mark) | Topic is related (1 mark) | Topic does not add to the subject knowledge much (0 Mark) | CO4 |

## List of Assignment for the Course Computer Organization and Architecture [12IS44]

| Sl No | Topic |
| :---: | :--- |
| 1. | Auxiliary memory -Working and advancements |
| 2. | 3-D Stacked Chips |
| 3. | Cellular Automata/Neural computation |
| 4. | Hyper threading |
| 5. | A Comparative Study on Mobile Phone Processors |
| 6. | Cellular Automata/Neural computation |
| 7. | INTEL's 50-core Xeon phi |
| 8. | Developments in Optical Computing |
| 9. | Computer Architecture based on multi value logic |
| 10. | Data flow architecture |

